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MESSAGE

Application No. 09/514,674
Filing Date February 28, 2000
First Named Inventor: William H. Robertson
Attorney Docket No. 248/225

Enclosures:

1. AFTER FINAL REQUEST FOR INTIALED FORM PTO/SB/08A (2 pgs)
2. Copy of PCT/SB/08A (1 pg)

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Patent
248/225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

William H. Robertson

Serial No.: 09/514,674

Filed: February 28, 2000

For: AUTOMATED METHOD AND
SYSTEM FOR SELECTING AND
PROCURING ELECTRONIC
COMPONENTS USED IN CIRCUIT AND
CHIP DESIGNS

Group Art Unit: 2812

Examiner: Stacy Whitmore

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Dear Sir:

On October 31, 2002, applicants submitted a Supplemental Information Disclosure Statement including two pages of Forms PTO/SB/08A along with the cited references. In the March 31, 2003 Office Action, the Examiner returned an initialed copy of the first page only.

Applicants respectfully request that the Form PTO/SB/08A as filed on October 31, 2002 enclosed herewith be initialed by the Examiner and returned to the applicants as soon as possible to complete our file before payment of the Issue Fee due March 8, 2004.

Patent
248/225

If the Examiner has any questions or comments, the Examiner is respectfully requested to contact the undersigned at the number listed below.


Respectfully submitted,

Bingham McCutchen LLP

Dated: February 5, 2004

By: Jeffrey S. Smith
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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
		Application Number	09/514,874
		Filing Date	February 28, 2000
		First Named Inventor	William H. Robertson et al.
		Group Art Unit	2182
		Examiner Name	Stacy A. Whitmore
Sheet 2 of 2	Attorney Docket Number	248/225	

OTHER DOCUMENTS - NON PATENT LITERATURE			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	9	Benini, L. et al., "Distributed EDA tool integration: the PPP paradigm," Proceedings of the International Conference on Computer Design, 10/1998, IEEE, pgs. 448-453	
	10	Blanchard, D., "EDA on the WWW PCB CAD," Printed Circuit Design, Miller Freeman, 02/1998, vol. 15, no. 2, pgs. 34-37	
	11	Chan et al., "WELD - An Environment for Web-Based Electronic Design," Proceedings of the 35 th Design Automation Conference, 05/1998, pgs. 146-151	
	12	Geppert, L., "The Rising Tide of Web-Based Tools and Services Augurs the Next Sea Change in Electronic Design IC Design on the World Wide Web," IEEE Spectrum, 06/1998, vol. 35, no. 6, pgs. 45-50	
	13	Moretti, "Got IP," Printed Circuit Design, Miller Freeman, 10/1998, vol. 15, no. 10, pgs. 31-33	
	14	Perrin, B., "Web-Based Circuit Engineering," Circuit Cellar Ink, 03/1999, no. 104, pgs. 26-30, 33, 35, 37-38	
	15	Schindler, P. et al., "IP repository: a Web based IP reuse infrastructure," Custom Integrated Circuits, 05/1999, IEEE, pgs. 415-418	
	16	Kondurt et al., "A Framework for Collaborative and Distributed Web-Based Design," Proceedings of the 36 th Design Automation Conference, 08/1999, pgs. 898-903	
	17	Wiseay, P., "Web-Based Analysis and Distributed IP," Proceedings of the 1999 Winter Simulation Conference, pgs. 1445-1453	
	18	Rao et al., "Web-Based Network Analysis and Design," ACM Transactions on Modeling and Computer Simulation, 01/2000, vol. 10, issue 1, pgs. 18-38	
	19	Wen et al., "Concurrent-Simulation-Based Remote IP Evaluation over the Internet for System-On-Chip Design," 14 th International Symposium on System Synthesis, 08/2001, pgs. 233-238	

Examiner Signature	Date Considered
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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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